REMARKS

This amendment is responsive to the Office Action dated January 30, 2002. Claims 16-25 are pending and subject to rejection. A Terminal Disclaimer to Obviate a Double Patenting Rejection Over a Prior Patent is enclosed, along with the required fee under 37 CFR §1.20(d). Since Applicant has removed the sole ground for rejection, the application is in allowable form.

This amendment is being filed withing the third month from the date of the Office Action. An Extension of Time is therefore unnecessary.

Please charge any additional fees or credit any overpayment to our Deposit Account No. 19-1995.

A duplicate copy of this letter is enclosed for that purpose.

I. <u>Double Patenting</u>

The Office Action rejects Claims 16-25 under the judicially created doctrine of non-statutory double patenting over Claims 1-5 of U.S. Patent No. 5,886,732. Although Applicants respectfully disagree with the assertion of obviousness in this case, in response to the non-statutory double patenting rejection, Applicants hereby file the attached Terminal Disclaimer in compliance with 37 CFR 1.321(c). This Terminal Disclaimer obviates the rejection. Thus, Applicants respectfully request the withdrawal of the rejection thereunder.

II. Claim Amendments

Applicant herewith makes a minor amendment to Claim 18 in conformity with the comments stated in the Office Action. Thus, the Claims are now in allowable form, and the objection in Claim 18 should be removed.

CONCLUSION

It is respectfully submitted that the case is now in condition for allowance, and an early notification of the same is requested. If it is believed that a telephone interview will help further the prosecution of this case, Applicants respectfully request that the undersigned attorney be contacted at the listed telephone number.

Dated: April 30, 2002

Respectfully submitted,

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CERTIFICATE OF MAILING

Evelyn Menjivar

(Type or print name of person mailing paper)
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(Signature of person mailing paper)



MARKED UP VERSION OF CLAIMS SHOWING CHANGES MADE

18. (Once Amended) The arrangement of claim 2, wherein the internal network interface of the network interface unit includes an MPEG to internal network synthesizer that converts clocking of the first signal from [and] an MPEG clocking to an internal network clocking of the second signal.

